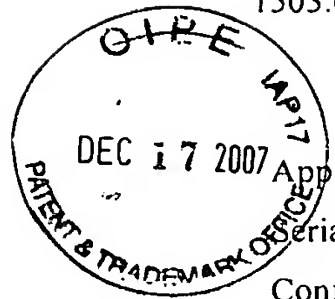


AF/TFW

1503.68591

PATENT APPLICATION

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE



Applicant: Makoto Nakanishi
Serial No.: 10/692,533
Conf. No.: 9889
Filed: 10/24/2003
For: PARALLEL PROCESSING METHOD
FOR INVERSE MATRIX FOR
SHARED MEMORY TYPE SCALAR
PARALLEL COMPUTER
Art Unit: 2193
Examiner: Mai, Tan V.

)
) I hereby certify that this paper is being deposited
) with the United States Postal Service as FIRST-
) CLASS mail in an envelope addressed to: Mail
) Stop AF, Commissioner for Patents, P.O. Box
) 1450, Alexandria, VA 22313-1450, on this date.
) December 12, 2007
) Date
) Registration No. 37,538
) Attorney for Applicant(s)

Mail Stop AF
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

AMENDMENT TRANSMITTAL

Dear Sir:

Transmitted herewith is a communication regarding the above-identified application.

Fee Calculation For Claims As Amended

	As Amended	Previously Paid For	Present Extra	Rate	Additional Fee
Total Claims	8	20	0	\$ 50.00	\$ 0
Independent Claims	2	3	0	\$210.00	\$ 0
Fee for Multiple Dependent Claims				\$370.00	\$ 0
				Total Additional Fee	\$ 0
				Small Entity Fee (reduced by half)	\$

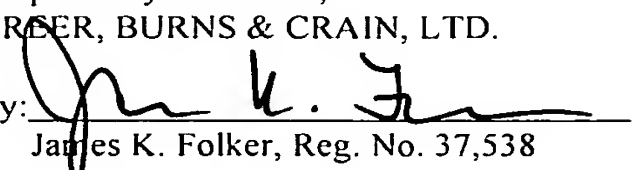
(X) Amendment B.

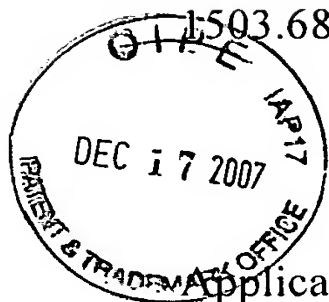
(X) If a Petition under 37 C.F.R. §1.136(a) for an extension of time for response is required to make the attached response timely and does not separately accompany this transmittal, Applicant(s) hereby petition(s) under 37 C.F.R. §1.136(a) for an extension of time for response in the above-identified application for the period required to make the attached response timely.

(X) The Commissioner is hereby authorized to charge any additional fees which may be required to this application under 37 C.F.R. §§1.16-1.17, or credit any overpayment, to Deposit Account No. 07-2069. A duplicate copy of this sheet is enclosed.

300 South Wacker Drive -- Suite 2500
Chicago, Illinois 60606
Tel.: (312) 360-0080
Fax: (312) 360-9315
Customer No.: 24978

Respectfully submitted,
GREER, BURNS & CRAIN, LTD.

By: 
James K. Folker, Reg. No. 37,538



1503.68591

PATENT APPLICATION

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Makoto Nakanishi

Serial No.: 10/692,533

Conf. No.: 9889

Filed: October 24, 2003

For: PARALLEL PROCESSING
METHOD FOR INVERSE
MATRIX FOR SHARED
MEMORY TYPE SCALAR
PARALLEL COMPUTER

Art Unit: 2193

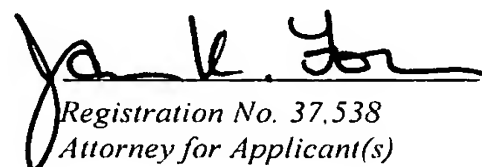
Examiner: Mai, Tan V.

) I hereby certify that this paper is being deposited with the
) United States Postal Service as FIRST-CLASS mail in an
) envelope addressed to: Mail Stop AF, Commissioner for
) Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on this
) date.

) December 12, 2007

) Date

) F-CLASS.WCM
) Appr. February 20, 1998


Registration No. 37,538
Attorney for Applicant(s)

AMENDMENT B

Mail Stop AF
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Dear Sir:

In response to the Office Action mailed September 24, 2007, please amend
the above-identified Application as follows: